



PHYSICAL DESIGN ENGINEER

Inova Semiconductors a Munich-based fabless semiconductor manufacturer founded in 1999, designs, markets and sells its products and licensed technologies both directly and through a global network of distributors, heavily focusing on the automotive market. The core competence of Inova resides in the development and marketing of high-speed digital data transmission semiconductor technologies for harsh automotive environments.

APIX™, APIX™2 and APIX™3 are the company's flagship product lines that have delivered major advancements in digital multimedia transmission, particularly in the automotive, industrial and transportation market segments. Inova Semiconductors has recently entered the emerging smart digital LED market under the ISELED™ brand name as member of an open industry-wide alliance.

JOB SUMMARY

- Responsible for the physical implementation of our mixed-signal semiconductors, focusing on synthesis, LEC and test insertion as well as the entire padframe generation.
- Carrying out chip level floorplanning, power estimation and IR drop analysis.
- Clock tree synthesis and optimization.
- Formal and physical verification.

KEY QUALIFICATIONS

- Degree in electrical engineering/communications engineering or a comparable field of study.
- Solid and up-to-date knowledge of the entire CADENCE and/or SYNOPSIS backend flow and MENTOR Calibre.
- Good practical and theoretical knowledge of signal integrity, EMC and mixed-signal designs.
- Experience in writing technical documentation.
- Solid knowledge of semiconductor process technology (CMOS) and semiconductor physics.
- Very good analytical skills and a high degree of assertiveness, commitment and motivation.
- Fluent in German and English.
- Minimal 3 - 5 years of professional experience.

CONTACT

Inova Semiconductors GmbH
Human Resources
Grafinger Str. 26
D-81671 Munich - Germany
Phone: +49-89-457475-60
Fax: +49-89-457475-88
hr@inova-semiconductors.de
www.inova-semiconductors.de

**inova**
Semiconductors